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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
10/598,151	06/07/2007	2819	1630	24179965-000006	13	6

## CONFIRMATION NO. 1657

23562  
BAKER & MCKENZIE LLP  
PATENT DEPARTMENT  
2001 ROSS AVENUE  
SUITE 2300  
DALLAS, TX75201

## FILING RECEIPT

Date Mailed: 09/25/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

### Applicant(s)

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### Assignment For Published Patent Application

TOKYO ELECTRON LIMITED, Tokyo, JP

**Power of Attorney:** The patent practitioners associated with Customer Number 23562

### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP05/03226 02/21/2005

### Foreign Applications

JAPAN 2004-042701 02/19/2004

### If Required, Foreign Filing License Granted: 09/19/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/598,151**

**Projected Publication Date:** 12/27/2007

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Programmable Logic Circuit Control Apparatus, Programmable Logic Circuit Control Method and Program

**Preliminary Class**

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